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(54) **XDSL REPEATER SYSTEM AND METHOD**

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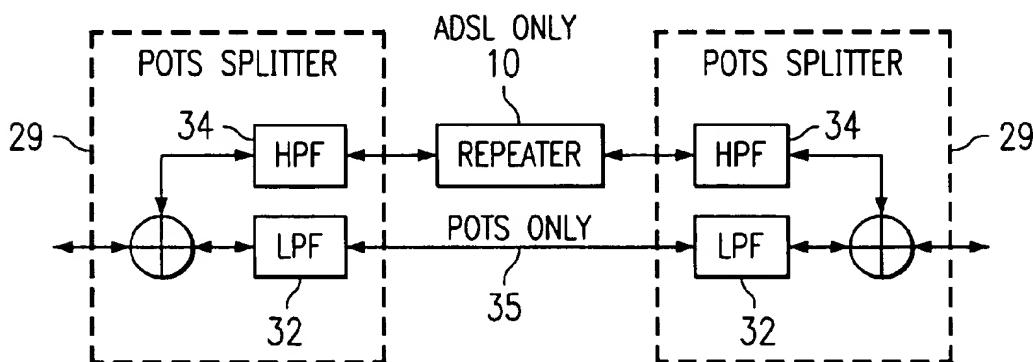
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(57) **ABSTRACT**

A system for repeating an xDSL signal (14) is disclosed. The system comprises a receiver (16) operable to receive the xDSL signal (14), a first analog front end (18) coupled to the receiver (16) and operable to convert the xDSL signal to a digital signal, and a series of digital signal processors (20, 22) coupled to the first analog front end (18) and operable to remove noise elements from the digital signal. Also provided is a second analog front end (24) coupled to the second digital signal processor (22) and operable to convert the digital signal back to the analog domain. A driver circuit (26) is operable to increase the signal strength of the analog signal and retransmits it over a new length of wiring.

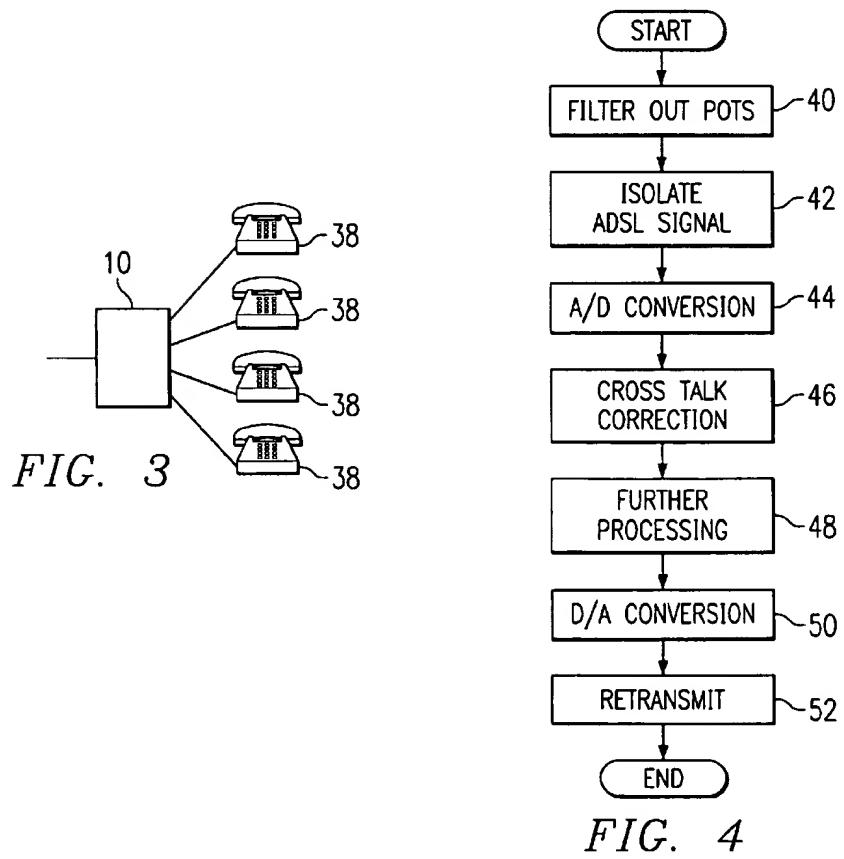
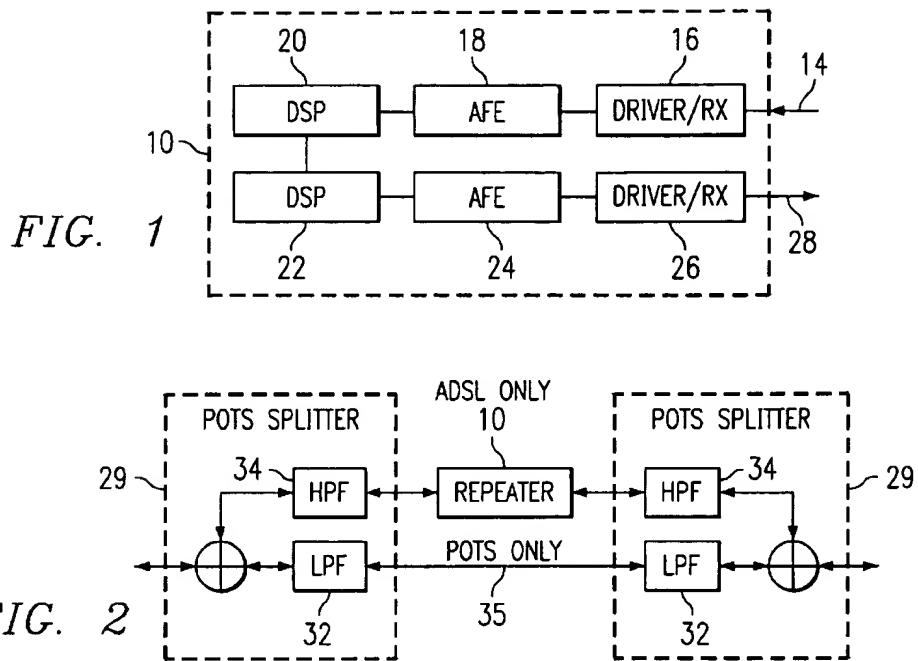
12 Claims, 1 Drawing Sheet



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**XDSL REPEATER SYSTEM AND METHOD****TECHNICAL FIELD OF THE INVENTION**

This invention relates to the field of telecommunication and more specifically to an xDSL repeater system and method.

**BACKGROUND OF THE INVENTION**

As demand for broadband services, such as high speed Internet access and on-demand video to the home increases, the need for technologies that can transmit large bandwidths of information also increases.

One technology being introduced to fill this need is digital subscriber lines (xDSL). The advantage of xDSL over other broadband technologies such as fiber optics to the home or cable modems is that it uses the existing twisted pair copper lines that already provide telephone services to homes.

The term xDSL is a catchall term covering a number of similar technologies. These include High bit rate Digital Subscriber Line (HDSL) and Asymmetric Digital Subscriber Line (ADSL).

HDSL is designed to deliver T1 (1.544 Mbps) and E1 (2.048 Mbps) services over conventional copper lines by using transceivers on each end of two or three twisted pairs. Single pair HDSL is limited to 384 Kbps or 768 Kbps symmetrical speeds.

ADSL uses conventional twisted pair copper lines to provide a large downstream path and a smaller upstream path. This allows for services such as on demand video and high speed Internet access where the user needs to receive greater bandwidth than the user needs to send. In one embodiment, ADSL is capable of providing downstream rates in excess of 6 Mbps and simultaneous duplex transmissions of 640 Kbps. Several competing ADSL standards exist. These include Discrete Multitone (DMT) and Carrierless Amplitude and Phase modulation (CAP). In one embodiment, DMT divides the 1 MHZ phone line spectrum into 256 4 KHz channels. Transmission bit density can be varied to overcome noise and interference.

CAP uses a single carrier and utilizes amplitude modulation similar to that used for modems to achieve ADSL transmission.

Both DMT and CAP operate by placing an ADSL modem on either end of a twisted-pair telephone line (one at a central office and the other at the customer's home or office). Three channels are created: a POTS (plain old telephone service) channel, a medium speed duplex channel and a high speed downstream channel. In a typical embodiment, POTS takes up the first 4 KHz of the phone line spectrum. The medium speed duplex channel and the high speed downstream channel occupy higher frequencies in the spectrum. Since the POTS channel is located in the first 4 KHz of the spectrum it can be split off from the data channels by filtering, thus providing an uninterrupted POTS connection.

One drawback of xDSL systems is that transmission signals attenuate as line length increases. For example, in order to maintain a downstream rate of around 6 Mbps the maximum loop length of copper is 12,000 Feet. This drawback limits the number of homes that can access xDSL service.

**SUMMARY OF THE INVENTION**

Accordingly, a need has arisen for an xDSL Repeater System and Method. The present invention includes a sys-

tem and method that provides a xDSL repeater that addresses the shortcoming of prior systems and methods.

According to one embodiment of the invention, a system for repeating an xDSL signal is provided. The system is comprised of two receiver/transmitters. The receivers consists of an analog front end which amplifies and converts the analog signal to the digital domain. The digital data is passed to a digital signal processor which is responsible for time and frequency domain equalization, echo cancellation, and decoding the signal. The transmitters take the decoded data and encode the data, transfer it to the analog front end where it is converted to an analog signal, amplified, and sent down the remainder of the cable to the far end transceiver. Also, a replica of the transmitted signal is transferred to the receiver DSP to remove crosstalk that is induced on the line when the powerful transmitted signal leaves the repeater adjacent to the weaker received signal.

The present invention provides various technical advantages. For example, a repeater is provided that allows for xDSL to reach further distances. Other technical advantages may be readily apparent to one skilled in the art from the following figures, descriptions and claims.

**BRIEF DESCRIPTION OF THE DRAWINGS**

For a more complete understanding of the present invention and advantages thereof, reference is now made to the following descriptions taken in conjunction with the following drawings, in which like numbers represent like parts, in which:

**FIG. 1** illustrates an xDSL repeater in accordance with the teachings of the present invention;

**FIG. 2** illustrates a high pass filter with a repeater in accordance with the teaching of the present invention;

**FIG. 3** illustrates an alternative embodiment of the present invention; and,

**FIG. 4** is a flowchart illustrating the operation of the present invention.

**DETAILED DESCRIPTION OF THE INVENTION**

Embodiments of the present invention and its advantages are best understood by referring to FIGS. 1 through 4 of the drawings, like numerals being used for like and corresponding parts of the various drawings.

**FIG. 1** illustrates a xDSL repeater in accordance with the teaching of the present invention. xDSL repeater 10 comprises a first driver/receiver 16 coupled to a first analog front end 18 coupled to a first digital signal processor (DSP) 20 which is coupled to second DSP 22 which is in turn coupled to a second analog front end 24 which is coupled to a second driver/receiver 26. FIG. 1 is a schematic drawing showing operational blocks for each, the various functional blocks could actually be implemented as one or more components in an actual embodiment.

In operation, a xDSL signal 14 is received by driver/receiver 16. Driver/receiver 16 then passes the signal to analog front end 18 which converts the analog signal to a digital signal in order for it to be processed by first DSP 20. First DSP 20 receives the digital signal from analog front end 18.

First DSP 20 and second DSP 22 are operable, either singularly or together, to eliminate crosstalk and other noise which can occur in a repeater system. Crosstalk occurs when there is a signal delay in a repeater which is a replica of the regenerated signal that overlays the transmitted signal.

Typically, the longer the loop length, the worse crosstalk can be. Crosstalk also can occur when a bundle of twisted pair wires are provided together. In this case, crosstalk occurs between the different wires in a bundle. First DSP 20 can eliminate crosstalk by monitoring what signal exits second DSP 22 and then finding a preset delay to produce a replica of the transmitted signal (crosstalk) to be subtracted from the received signal. As an alternate, the first DSP 20 can take the received signal, generate its own version of the crosstalk replica and subtract the delayed version from its own input.

Aside from handling crosstalk, DSP 20 and 22 can also perform other signal processing chores such as time or frequency domain transformations, block error corrections using algorithms such as the Reed-Solomon algorithm, or bit error corrections using a Viterbi algorithm or other bit correcting algorithms. These can be done for noise reduction purposes.

After DSPs 20 and 22 have eliminated crosstalk and performed other signal processing chores, the signal is sent to second analog front end 24. Analog front end 24 converts the digital signal back to an analog signal which is then sent to driver 26. Driver 26 then transmits the regenerated signal 28 over the next loop of twisted pair wires.

In some cases of extreme crosstalk, analog front end 18 may need to be used to reduce crosstalk in the analog domain by subtracting the analog replica of the outgoing signal.

Additionally, since xDSL lines are capable of carrying a POTS signal, it is important to split off the POTS signal before sending the signal into repeater 10. FIG. 2 illustrates an exemplary system to filter out POTS signals. POTS Splitter 29 comprises a low pass filter 32 which will allow only frequencies below a certain level to pass. Low pass filter is chosen to allow only the POTS signal to travel along line 35. POTS splitters 29 are set up on either side of repeater 10 and line 35 since communication can travel in either direction. POTS splitter 29 optionally includes a high pass filter 34 placed before the repeater to ensure only frequencies above the POTS frequency is sent to repeater 10.

FIG. 3 illustrates another embodiment of the present invention. In FIG. 3 the incoming digital signal 14 is sent to repeater 10. In this case, repeater 10 is operable to repeat the signal at lower bandwidth rates and send them along different paths to individual homes 38. This acts like a multiplexer. Like a repeater, it also provides a longer line length, albeit it at a lower transmit and receive rate than a single xDSL loop would have. For example, a six megabit per second link could be divided among several households at a lower rate such as a 750 KHz downstream rate for each home. This approach can also be used in apartment complexes where one xDSL link could be used to provide many subscribers in different apartments with slower rate xDSL lines.

FIG. 4 is a flow chart illustrating the operation of the present invention. As execution of the flow chart begins in step 40, the POTS line is filtered out. As discussed before, this can be done by means of a high pass filter or other filtering techniques to remove lower frequency POTS line from the upstream and downstream data signals of xDSL. In step 42, the xDSL signal is isolated. Then, in step 44, the analog front end completes an analog digital conversion converting the analog data signals into the digital domain. Crosstalk is canceled by DSPs 20 and 22 in step 46. This can be done as discussed before in several different ways.

The signal is then further processed in step 48. This further processing can include error correction via a block-

ing coding process such as a Reed-Solomon process or a bit incrementing process such as that done in Viterbi analysis. Also, time or frequency based transforms may be done for signal processing purposes. Then, in step 50, the digital signal is converted back to the analog domain and in analog front end. Finally, in step 52, the signal is then boosted using driver 26 and the now analog signal is repeated and sent over a new loop of twisted pair copper wiring until it reaches either a customer presence equipment or another repeater.

Although the present invention has been described in detail, it should be understood that various changes, substitutions and alterations can be made thereto without departing from the spirit and scope of the invention as defined by the appended claims.

What is claimed is:

1. A method for repeating an xDSL signal comprising the steps of:  
isolating the xDSL signal;  
converting the xDSL signal into a digital signal;  
removing noise elements;  
converting the signal back to an analog signal;  
retransmitting the signal; and  
wherein the step of removing noise elements comprises  
removing crosstalk by formulating an appropriately  
delayed signal image.

2. A method for repeating an xDSL signal comprising the steps of:

- isолating the xDSL signal;  
converting the xDSL signal into a digital signal;  
removing noise elements;  
converting the signal back to an analog signal;  
retransmitting the signal; and  
wherein the step of removing noise elements comprises  
performing bit error corrections.

3. A system for repeating an xDSL signal comprising:  
a receiver operable to receive the xDSL signal;

- a first analog front end coupled to the receiver and  
operable to convert the xDSL signal to a digital signal;  
a series of digital signal processors coupled to the first  
analog front end and operable to remove noise elements  
from the digital signal;

a second analog front end coupled to each digital signal  
processor and operable to convert the digital signal  
back to an analog xDSL signal; and

- a driver circuit coupled to each analog front end and  
operable to increase the signal strength of the analog  
xDSL signal and retransmit it over a new length of  
wiring.

4. The system of claim 3, wherein a POTS signal is  
removed before noise elements are removed.

5. The system of claim 3, wherein the digital signal  
processors are operable to remove noise elements by remov-  
ing crosstalk by formulating an appropriately delayed rep-  
lica.

6. The system of claim 3, wherein the digital signal  
processors are operable to perform block error corrections.

7. The system of claim 3, wherein the digital signal  
processors are operable to perform bit error corrections.

8. The system of claim 3, wherein the first and second  
analog front ends are operable to remove crosstalk.

9. The system of claim 3, wherein the xDSL signal is an  
ADSL signal.

10. A system for repeating an xDSL signal comprising:  
a receiver operable to receive the xDSL signal;

a first analog front end coupled to the receiver and operable to convert the xDSL signal to a digital signal;  
 a series of digital signal processors coupled to the first analog front end and operable to remove noise elements from the digital signal; <sup>5</sup>  
 a second analog front end coupled to each digital signal processor and operable to convert the digital signal back to the analog domain;  
 a driver circuit coupled to each analog front end and operable to increase the signal strength of the analog signal and retransmit it over a new length of wiring; and wherein the digital signal processors are operable to remove noise elements by removing crosstalk by formulating an appropriately delayed replica. <sup>10</sup>

11. A system for repeating an xDSL signal comprising:  
 a receiver operable to receive the xDSL signal;  
 a first analog front end coupled to the receiver and operable to convert the xDSL signal to a digital signal; <sup>15</sup>  
 a series of digital signal processors coupled to the first analog front end and operable to remove noise elements from the digital signal;  
 a second analog front end coupled to each digital signal processor and operable to convert the digital signal back to the analog domain; <sup>20</sup>

a driver circuit coupled to each analog front end and operable to increase the signal strength of the analog signal and retransmit it over a new length of wiring; and wherein the digital signal processors are operable to perform bit error corrections.  
 12. A system for repeating an xDSL signal comprising:  
 a receiver operable to receive the xDSL signal;  
 a first analog front end coupled to the receiver and operable to convert the xDSL signal to a digital signal;  
 a series of digital signal processors coupled to the first analog front end and operable to remove noise elements from the digital signal;  
 a second analog front end coupled to each digital signal processor and operable to convert the digital signal back to the analog domain;  
 a driver circuit coupled to each analog front end and operable to increase the signal strength of the analog signal and retransmit it over a new length of wiring; and wherein the first and second analog front ends are operable to remove crosstalk. <sup>15</sup>

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